REMARKS

Reconsideration and allowance in view of the foregoing amendments and the following remarks are respectfully requested.

Claim 5 has been amended. Claims 1-9 are pending in this application.

Claim 5 stands rejected under 35 U.S.C. §112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which Applicant regards as the invention. In response, Applicant has amended claim 5 by changing the order of the signals. Applicant believes that claim 5 now recites a proper order of the signals and is in full compliance with 35 U.S.C. §112, second paragraph. Withdrawal of the rejection is respectfully requested.

Applicant notes with appreciation that claims 1-4 are allowed.

The Examiner stated that claim 5 would be allowable if rewritten or amended to overcome the rejection under 35 U.S.C. §112, second paragraph, set forth in this Office Action. As set forth above, claim 5 is now in full compliance with 35 U.S.C. §112, second paragraph. Accordingly, it is believed that claim 5 is now allowable.

With respect to dependent claims 6-9 which are objected to as being dependent upon a rejected base claim, Applicant submits that these claims are now allowable as claim 5 is now allowable.

3

Attached hereto is a marked-up version of the changes made to the claims by the current Amendment. The attached page is captioned "Version with Markings to Show Changes Made."

All objections and rejections having been addressed, it is respectfully submitted that claims 1-9 are now in condition for allowance and a notice to that effect is earnestly solicited. If any issues remain to be resolved, the Examiner is cordially invited to telephone the undersigned attorney at the number listed below.

Respectfully submitted,

By: _____
Yoon S. Ham
Reg. No. 45,307
Direct Tel.: (202) 662-8483

JACOBSON HOLMAN, PLLC
The Jenifer Building
400 Seventh Street, N.W.
Washington, D.C. 20004-2201
(202) 638-6666

Atty. Docket: P66470US0
YSH:dj

4

<u>**VERSION WITH MARKINGS TO SHOW CHANGES MADE**</u>

<u>IN THE CLAIMS</u>:

Please amend claim 5 as follows:

5. (Twice Amended) A decoder circuit in a flash memory device, comprising:

a global row decoder for outputting a global word line signal, wherein said global row decoder comprises:

a first transistor for transferring a first voltage to a node according to a first signal;

a second transistor for transferring a ground voltage to said node according to said first signal;

a third transistor for transferring a second voltage to a global word line according to potential of said node; and

a fourth transistor for transferring said first voltage to said global word line according to potential of said node;

a local row decoder for selecting a word line in response to said global word line signal of said global row decoder, wherein said local row decoder comprises:

a fifth transistor for transferring said global word line signal to said word line in response to a [third] <u>second</u> signal;

a sixth transistor for transferring said global word line signal to said word line according to a [fourth] <u>third</u> signal; and

5

JUN 0 2 2003

OFFICE OF PETITIONS

T77

a seventh transistor for transferring a ground voltage to said
word line in response to [a fifth signal] said second signal.

SuCOM